



**LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING
(AUTONOMOUS)**

L.B. Reddy Nagar :: Mylavaram-521 230 :: Krishna Dist. :: A.P
Approved by AICTE, New Delhi. Affiliated to JNTUK, Kakinada
Accredited by NAAC with "A" Grade & ISO 9001: 2015 Certified
EXAMINATION SECTION

REVISED

I MID EXAMINATION

**B.Tech.(III-Semester) (R17)
(2017 Admitted Batch)**

A.Y. 2018-2019

TIME TABLE

TIME : 9.30 AM – 11.00 AM

DATE	AE	CE	CSE	ECE	EEE	EIE	IT	ME
30-07-2018 (Monday)	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE08 - Probability and Statistics	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis
31-07-2018 (Tuesday)	17AE01 - Engineering Fluid Mechanics	17EE51 - Fundamentals of Electrical Engineering	17FE03 - Environmental Science	17CI01 - Computer Programming	17EE02 - Electric and Magnetic Fields	17FE03 - Environmental Science	17FE03 - Environmental Science	17FE03 - Environmental Science
01-08-2018 (Wednesday)	17AE02 - Engineering Thermodynamics	17CE04 - Strength of Materials - I	17CI03 - Discrete Mathematical Structures	17EC05 - Signals and Systems	17EE03 - Network Theory - I	17EE53 - Electrical Technology	17CI07 - OOPs through Java	17EC50 - Basic Electronics Engineering
02-08-2018 (Thursday)	17AE03 - Strength of Materials	17CE05 - Engineering Geology	17CI04 - Python Programming	17EC06 - Random Variables and Stochastic Processes	17EE04 - Digital Logic Circuit Design	17E02 - Transducers	17IT01 - Operating System Principles	17ME03 - Thermodynamics
03-08-2018 (Friday)	17AE04 - Elements of Aerospace Engineering	17CE06 - Mechanics of Fluids	17CI05 - Data Structures	17EC07 - Pulse and Switching Circuits	17CI05 - Data Structures	17EC03 - Analog Electronic Circuits	17CI02 - Digital Logic Design	17ME04 - Mechanics of Solids
04-08-2018 (Saturday)	17ME05 - Metallurgy and Material Science	17CE07 - Concrete Technology	17CI06 - Computer Architecture	17EC08 - Analog Integrated Circuits	17EE05 - Power Generation and Utilization	17EC04 - Digital Electronic Circuits	17CI09 - Data Base Management Systems	17ME05 - Metallurgy and Material Science
06-08-2018 (Monday)	17PD03 - Professional Ethics and Human Values	17PD03 - Professional Ethics and Human Values	---	17PD03 - Professional Ethics and Human Values	17PD03 - Professional Ethics and Human Values	---	---	---

Date: 25-07-2018

CONTROLLER OF EXAMINATIONS

PRINCIPAL

- Copy to:
1. All Notice Boards
 2. All H.O.Ds for N.A.,



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**B.Tech.(III-Semester)(R17)
(2017 Admitted Batch)**

A.Y. 2018-2019

**ONLINE QUIZ EXAMINATION - I
TIME TABLE**

DATE	AE	CE	CSE	ECE	EEE	EIE	IT	ME
30-07-2018 (Monday)	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE08 - Probability and Statistics	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis
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03-08-2018 (Friday)	17AE04 - Elements of Aerospace Engineering	17CE06 - Mechanics of Fluids	17CI05 - Data Structures	17EC07 - Pulse and Switching Circuits	17CI05 - Data Structures	17EC03 - Analog Electronic Circuits	17CI02 - Digital Logic Design	17ME04 - Mechanics of Solids
04-08-2018 (Saturday)	17ME05 - Metallurgy and Material Science	17CE07 - Concrete Technology	17CI06 - Computer Architecture	17EC08 - Analog Integrated Circuits	17EE05 - Power Generation and Utilization	17EC04 - Digital Electronic Circuits	17CI09 - Data Base Management Systems	17ME05 - Metallurgy and Material Science
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EXAMINATION SECTION

Phone & Fax.:08659-223498

examsection@lbrce.ac.in

Date : 23-07-2018

**B.Tech.(III-Semester)(R17)
(2017 Admitted Batch)**

A.Y. 2018-2019

ONLINE QUIZ EXAMINATION - I

TIME TABLE

Following is the schedule for B.Tech.(III Semester)(R17 batch) online quiz examination-I.

DATES	BRANCH		VENUE
	SLOT-1 (12.15 p.m - 12.45 p.m)	SLOT-2 (12.45p.m - 01.15 p.m)	
30-07-2018 To 06-08-2018	CSE (A/S)	--	IPC lab (Block-I) Ground Floor
	CSE (B/S)	--	IT workshop (Block-I) Ground Floor
	--	IT	CDS lab (Block-I) First Floor
	--	ME (C/S)	IT lab (Block-I) First Floor
	EEE (A/S)	EEE (B/S)	Computational lab (Block-II) First Floor
	ME (A/S)	ME (B/S)	CAD/CAM lab (Block-III) Ground Floor
	ECE (A/S)	ECE (B/S)	SP lab (Block-IV) Second Floor
	EIE	ECE (C/S)	Systems lab (Block-V) Ground Floor
	CE	AE	English lab (Block-V) First Floor


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